## UNIVERSITI KUALA LUMPUR

Malaysia France Institute

## FINAL EXAMINATION SEPTEMBER 2014 SESSION

| SUBJECT CODE | $:$ FLB23043 |
| :--- | :--- |
| SUBJECT TITLE | $:$ DIGITAL SYSTEM |
| LEVEL | $:$ BACHELOR |
| TIME / DURATION | $: 3.00$ PM - 6.00 PM |
|  | $(3$ HOURS) |
| DATE | $: 09$ JANUARY 2015 |

INSTRUCTIONS TO CANDIDATES

1. Please read the instructions given in the question paper CAREFULLY.
2. This question paper is printed on both sides of the paper.
3. Please write your answers on the answer booklet provided.
4. Answers should be written in blue or black ink except for sketching, graphic and illustration.
5. This question paper consists of TWO (2) sections. Section A and B. Answer all questions in Section A. For Section B, answer two (2) questions only.
6. Answer all questions in English.

THERE ARE 4 PAGES OF QUESTIONS, EXCLUDING THIS PAGE AND APPENDIX.

## SECTION A (Total: 60 marks)

## INSTRUCTION: Answer ALL questions.

Please use the answer booklet provided.

## Question 1

(a) Perform the Octal multiplication on $237_{8}+63_{8}$. Then perform the following on the answer obtained.
i. Convert it to binary
ii. Divide it with $10101_{2}$
(b) Subtract using the 2's complement binary subtraction: $-50_{10}-60_{10}$
(c) Calculate $\mathrm{C}_{2} \mathrm{~A}_{16}-\mathrm{A} 8 \mathrm{D}_{16}$. Convert the answer in decimal

## Question 2

For the sentence below:
"The alarm will ring if the alarm switch is turned on, and the door is not closed, or it is after 8 pm and the window is not closed".
(a) Represent its equivalent SOP expression
(b) Simplify the expression using Karnaugh map
(c) Produce the truth table for the simplified expression in (b)
(d) Draw the logic circuit for (b) using NOR gates only

## Question 3

(a) Simplify the following Boolean expression using De Morgan's theorem and/or Boolean algebra. Draw its simplified circuit.

$$
\begin{equation*}
M=(a+b)(\bar{a}+\bar{b}) \tag{6marks}
\end{equation*}
$$

(b) Simplify the following Boolean expression, in sum-of-products form, using K-map. Draw its simplified circuit

$$
F(w, x, y, z)=\sum_{m}(0,2,3,5,6,7,8,9)+D(10,11,12,13,14,15)
$$

(c) Produce the truth table of the following Boolean expression. Draw its simplified circuit.

$$
\begin{equation*}
I=(p+\bar{q}+r)(p+\bar{q}+\bar{r})(\bar{p}+q+r)(\bar{p}+\bar{q}+\bar{r}) \tag{6marks}
\end{equation*}
$$

## Question 4

Figure 1 shows two 4 X 1 multiplexer implemented as full adder. Determine the outputs functions $A$ and $B$ as sums of minterms. You may use any process to determine the result, but show your work.


Figure 1

## SECTION B (Total: 60 marks)

## INSTRUCTION: Answer only TWO (2) questions

Please use the answer booklet provided.

## Question 5

Design a synchronous counter using JK flip-flop with sequence as shown in the state diagram of Figure 2. The design should include the following:
(a) Excitation table
(b) Karnaugh map
(c) Circuit diagram


Figure 2

## Question 6

Design a magnitude comparator circuit for 2-bit binary numbers $\mathrm{A}=\mathrm{A} 1 \mathrm{~A} 0$ and $\mathrm{B}=\mathrm{B} 1 \mathrm{~B} 0$. The outputs are $F, G$, and $H$, where $F$ is 1 if $A>B, G$ is 1 if $A=B$, and $H$ is 1 if $A<B$.
(a) Fill in its truth table for the three output of the comparator (12 marks)
(b) Determine the output functions as sum of minterms (8 marks)

## Question 7

(a) Design a MOD- 6 up asynchronous counter using JK flip-flop with NGT clock. The design should include the following:
i. Counting sequence (3 marks)
ii. Circuit diagram. (7 marks)
iii. Output expressions at each flip-flop (3 marks)
(b) Construct a 2-to-4-line decoder by using a 1-to-4 demultiplexer. Use block diagrams.

## END OF QUESTION PAPER

## APPENDIX

## Boolean algebra and De Morgan's theorems

1. $X \bullet 0=0$
2. $X+\bar{X}=1$
3. $X+X Y=X$
4. $X \bullet 1=X$
5. $X+Y=Y+X$
6. $X \bullet X=X$
7. $X \bullet Y=Y \bullet X$
8. $X \bullet \bar{X}=0$
9. $X+(Y+Z)=(X+Y)+Z=X+Y+Z$
10. $X+0=X$
11. $X(Y Z)=(X Y) Z=X Y Z$
12. $X+1=1$
13a. $X(Y+Z)=X Y+X Z$
13. $X+X=X$
13b. $(W+X)(Y+Z)=W Y+X Y+W Z+X Z$
